

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Marco Zuniga
Serial No. : 09/475,713
Filed : December 30, 1999
Title : SWITCHING REGULATOR WITH MULTIPLE POWER TRANSISTOR
DRIVING VOLTAGES

Art Unit : 2838
Examiner : Shawn Riley

Commissioner for Patents
Washington, D.C. 20231

RESPONSE

In response to the action mailed February 23, 2001, please amend the application as follows:

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In the claims:

Please amend claims as follows:

1. A voltage regulator having an input terminal and an output terminal, comprising:
a first transistor connecting the input terminal to an intermediate terminal, the first transistor including a first gate oxide layer;
a second transistor connecting the intermediate terminal to ground, the second transistor including a second gate oxide layer;
a controller that drives the first and second transistors to alternately couple the intermediate terminal between the input terminal and ground, wherein the controller drives the first transistor with a first gate voltage and drives the second transistor with a second, different gate voltage; and
a filter disposed between the input terminal and the output terminal to provide a substantially DC voltage at the output terminal, the filter including at least one element connecting the intermediate terminal to the output terminal.

CERTIFICATE OF TRANSMISSION BY FACSIMILE

I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office on the date indicated below.

Date of Transmission May 21, 2000

Signature Matilde C. Sanchez

Typed or Printed Name of Person Signing Certificate Matilde C. Sanchez